**COMPUTER SCIENCE**  **STD 12**  **Video Assignment – 14**

**Q1**. Draw the logic circuit diagram of :

    i.  Hexadecimal to binary Encoder.
    ii. Base 4 to binary encoder.
    iii. Decimal to binary Encoder.

[ **Note:** Since no new topic was started, so practice the questions which were given in the previous assignment. In the next class, we shall learn about decoders. Thank you.]